



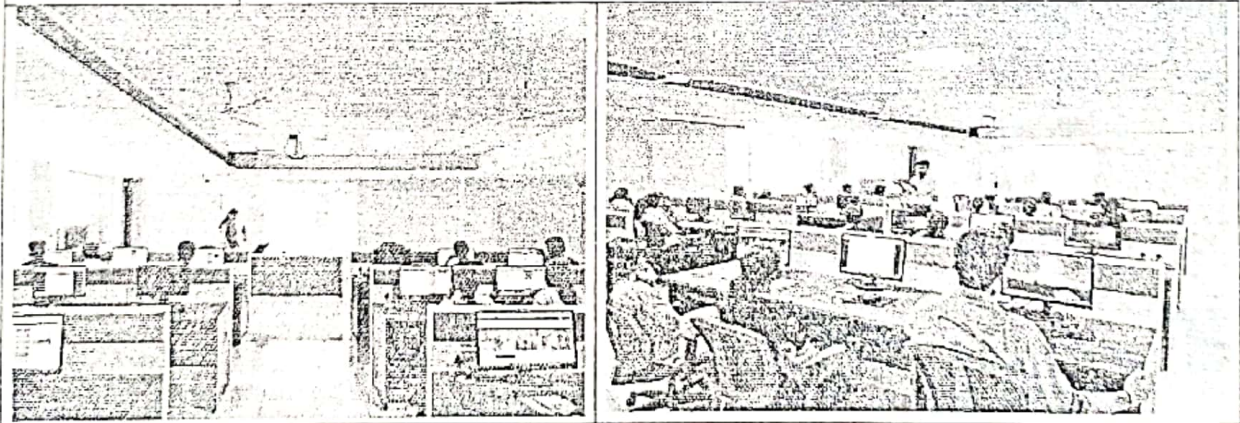
SOLVE the virtual labs at NITK Surathkal

(An Initiative of Ministry of Human Resource & Development
Under the National Mission on Education through ICT)

Participating Institute: National Institute of Technology Karnataka (NITK) Surathkal
www.vlab.co.in or www.vlabs.ac.in



Virtual lab workshop

Date	05-04-2019	
Institute Name	AJ Institute of Engineering and Technology, Mangalore	
Venue	AJ Institute of Engineering and Technology, Mangalore	
Resource Persons	Shreshta, Akshaya, Anusha, Aishwarya Shetty, Aishwarya Hegde, Aquib A Nadaf	
Participants	Branch	Total number of Faculty and students attended
	ECE	62
	CSE	81
	ISE	37
Content Covered	Introduction on VLabs, V-Lab simulation, Remote trigger Lab, Hands-on. (Basic Electronics Lab, Hybrid Electronics Lab, Analog Signals Network and Measurement Lab, Digital Electronic Circuits Lab, Digital Signal Processing Lab, Virtual Electric Circuits Lab, Computer Architecture & Organization Lab, Computer Organization Lab, Computer Programming Lab, Data Structures Lab, Image Processing Lab, Problem Solving Lab, SOM, MOM, Data structures, RT lab Vibration and SOM.)	
		

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Solve The Virtual Labs at Centre for System Design ,NITK Surathkal ,





John Veigas <john.veigas@ajiet.edu.in>

Virtual Lab Workshop Acknowledgement - AJIET, Mangalore

2 messages

Mon, Apr 8, 2019 at 11:36 AM

CSD CSD <csd@nitk.edu.in>
To: John Veigas <john.veigas@ajiet.edu.in>
Cc: "Dr. Gangadharan K V" <kvganga@gmail.com>

Respected Sir,

Greetings from Virtual Lab team, NITK.

Report on Virtual Lab workshop conducted at AJ Institute of Engineering & Technology, Mangalore on 5th April, 2019 for the 2nd and 3rd year students and faculty members of Department of Electronics & Communication Engineering, Computer Science & Engineering and Information Science & Engineering is attached below as requested by you for your reference. Please find the attachment.

We also request you to provide the convenient date for the Virtual Lab workshop for Mechanical and civil engineering faculty and students of AJIET, Mangalore. Kindly do the needful in this regard.

Thanks & Regards,

Centre for System Design (CSD)

A Trans disciplinary Center of Excellence
NITK Surathkal , Mangalore , Karnataka, India 575025
Office phone +91 824 2473915
Head : Dr. K V Gangadharan
<http://scholar.google.co.in/citations?hl=en&user=hk702CEAAAAJ>
<http://mech.nitk.ac.in/faculty/k-v-gangadharan>

Virtual lab workshop report-AJIET.pdf
310K

Mon, Apr 8, 2019 at 3 14 PM

John Veigas <john.veigas@ajiet.edu.in>
To: CSD CSD <csd@nitk.edu.in>

Thank you for providing report

With Regards

John Prakash Veigas
Asst. Professor
Dept. of Information Science and Engg.
AJ Institute of Engineering and Technology
Mangalore

[Quoted text hidden]

Virtual Lab NITK - Internship Programme 2019 - AJIET, Mangalore

1 message

CSD CSD <csd@nitk.edu.in>
To: John Veigas <john.veigas@ajiet.edu.in>
Cc: "Dr. Gangadharan K V" <kvganga@gmail.com>

Thu, May 9, 2019 at 12:40 PM

Respected Sir/Madam,

Greetings from Virtual Lab team, NITK.

Thank you for being a Nodal Center of Virtual Lab NITK for the year 2019.

We are happy to inform that, as your institute is one of the Nodal Center of Virtual Lab NITK, your institute is offered to send four smart and motivated students with outstanding academic performance, each from different background (preferably Mechanical, Civil, Electronics and Computer Science Engineering) to work for the Virtual Lab project under Virtual Lab Internship Programme 2019 for a period of 30 days to 45 days in this vacation. Its an opportunity exclusively provided for the students of active Nodal Centers. In this regard, you can send the detailed list of probable students who are interested to be a part of this programme at the earliest. Also, an internship application form attached need to be filled by each of the students and the soft copy of filled application form should be mailed to csd@nitk.edu.in. Please find the attachment below.

Feel free to contact us for any further information or clarification.

Thanks and regards,

Centre for System Design (CSD)


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<http://mech.nitk.ac.in/faculty/k-v-gangadharan>

 Virtual Lab_Internship_application.docx
268K